

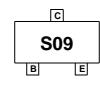


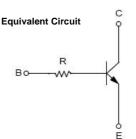
FJY3009R NPN Epitaxial Silicon Transistor

Features

- · Switching circuit, Inverter, Interface circuit, Driver Circuit
- Built in bias Resistor (R=4.7KΩ)
- Complement to FJY4009R







July 2007

Absolute Maximum Ratings * T_a = 25°C unless otherwise noted

Symbol	Parameter	Value	Units	
V _{CBO}	Collector-Base Voltage	40	V	
V _{CEO} Collector-Emitter Voltage		40	V	
V _{EBO} Emitter-Base Voltage		Emitter-Base Voltage 5		
I _C Collector Current T _{STG} Storage Temperature Range				
				TJ
P _C	Collector Power Dissipation, by $R_{\theta JA}$	200	200 mW	

These ratings are limiting values above which the serviceability of any semiconductor device may by impaired.

Thermal Characteristics* T_a=25°C unless otherwise noted

Symbol	Parameter	Мах	Units
R_{\thetaJA}	Thermal Resistance, Junction to Ambient	600	°C/W

Minimum land pad size.

Electrical Characteristics* T_c = 25°C unless otherwise noted

Symbol	Parameter	Test Condition	MIN	Тур	MAX	Units
V(BR)CBO	Collector-Emitter Breakdown Voltage	Ic = 100 uA, IE = 0	40			V
V(BR)CEO	Collector-Base Breakdown Voltage	Ic = 1mA, I _B = 0	40			V
Ісво	Collector-Cutoff Current	Vcb = 30 V, IE = 0			0.1	uA
hfe	DC Current Gain	Vce = 5 V, Ic = 1 mA	100		600	
Vce(sat)	Collector-Emitter Saturation Voltage	Ic = 10 mA, Iв = 1 mA			0.3	V
f⊤	Current Gain - Bandwidth Product	Vce = 10V, lc = 5 mA		250		MHz
Ccb	Output Capacitance	Vcb = 10 V, IE = 0, f = 1.0 MHz		3.7		pF
R	Input Resistor		3.2	4.7	6.2	KΩ

* Pulse Test: PW≤300µs, Duty Cycle≤2%

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Typical Performance Characteristics

Figure 1. DC current Gain

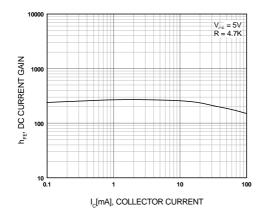


Figure 2. Collector-Emitter Saturation Voltage

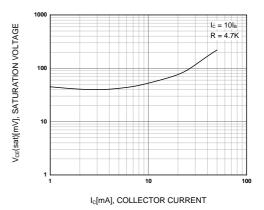
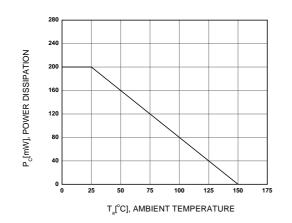
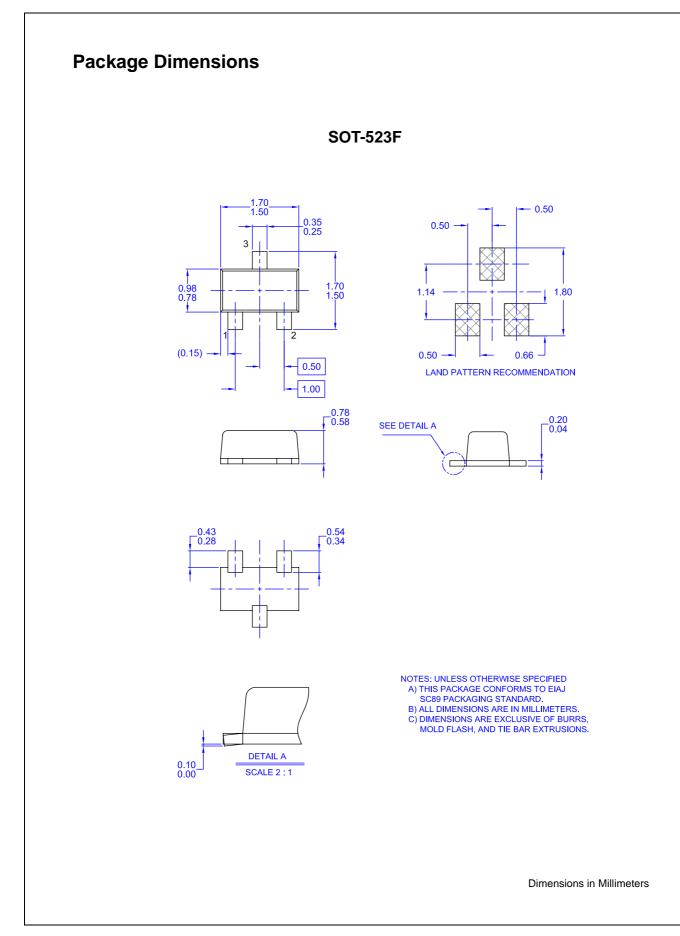


Figure 3. Power Derating





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